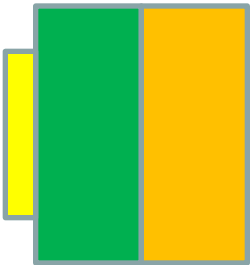


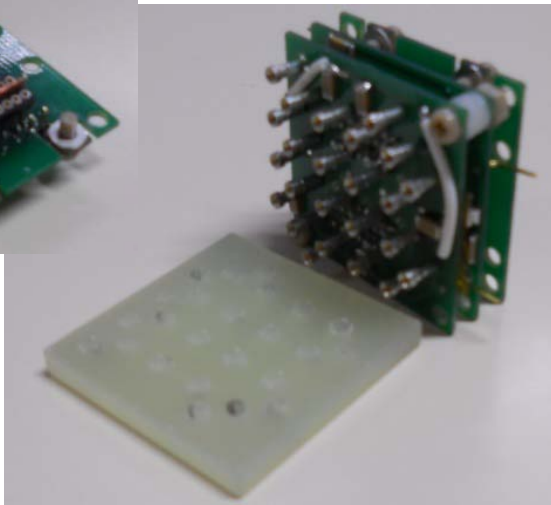
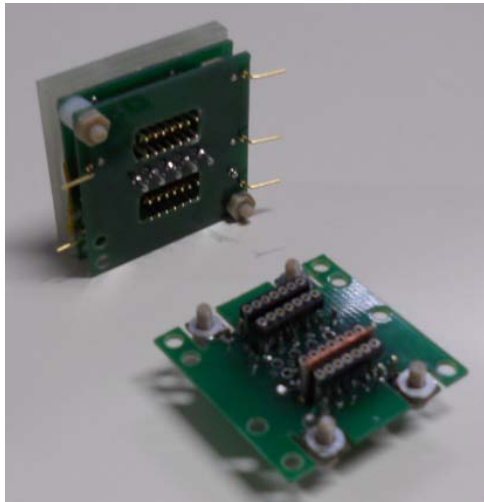
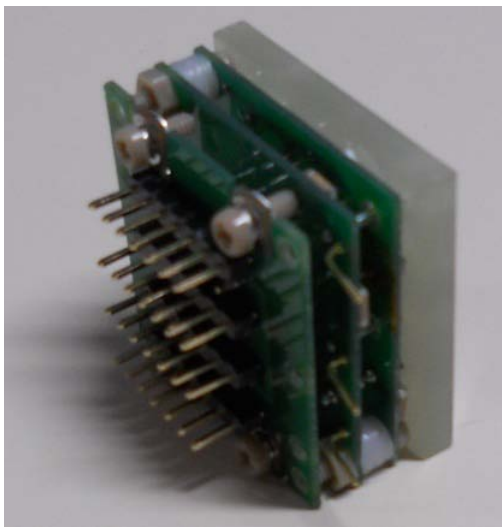
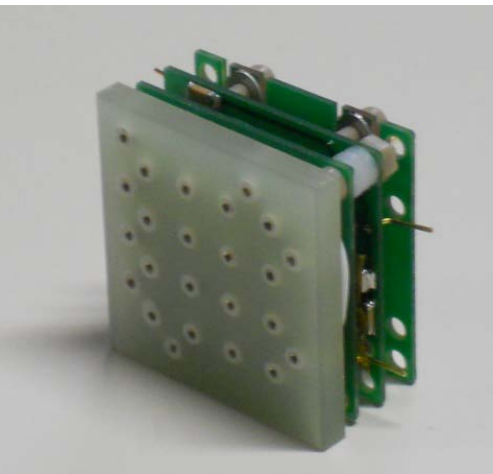
PMT socket (v3)

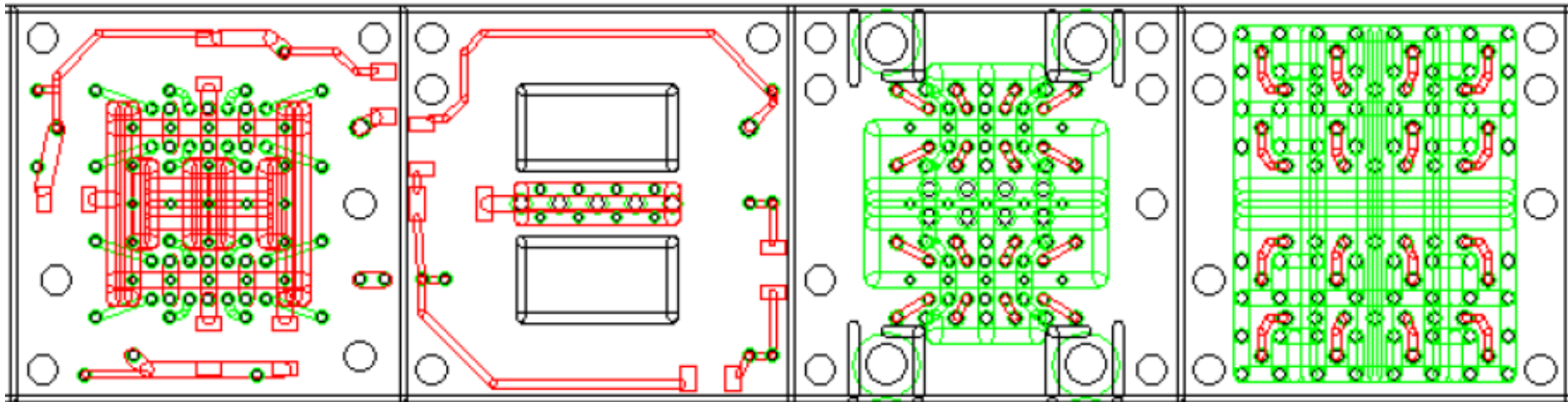
- Capacitors on board
- 5 HV cables ($\sim 1.1\text{mm}\phi$) $\rightarrow 40\text{mm}^2$ for 8 PMT
- Depth flexibility between first and third layer
 - Trial of PMT surface alignment
- 2mm-pitch socket for signal line
- HV supplied upto 3.8kV. OK
 - Potting for further protection

14mm 13.1mm+2.5mm



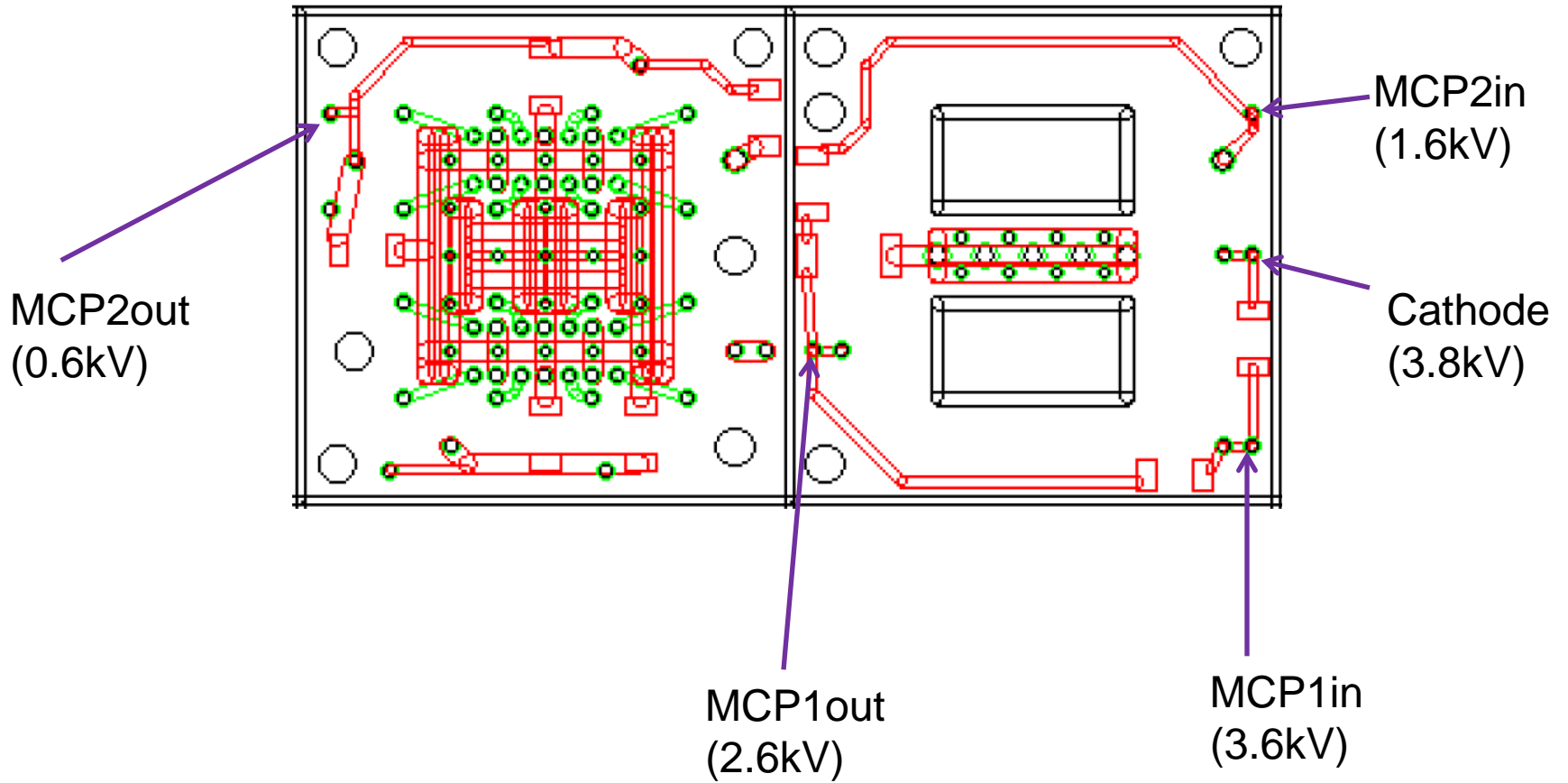
28mm+pin socket





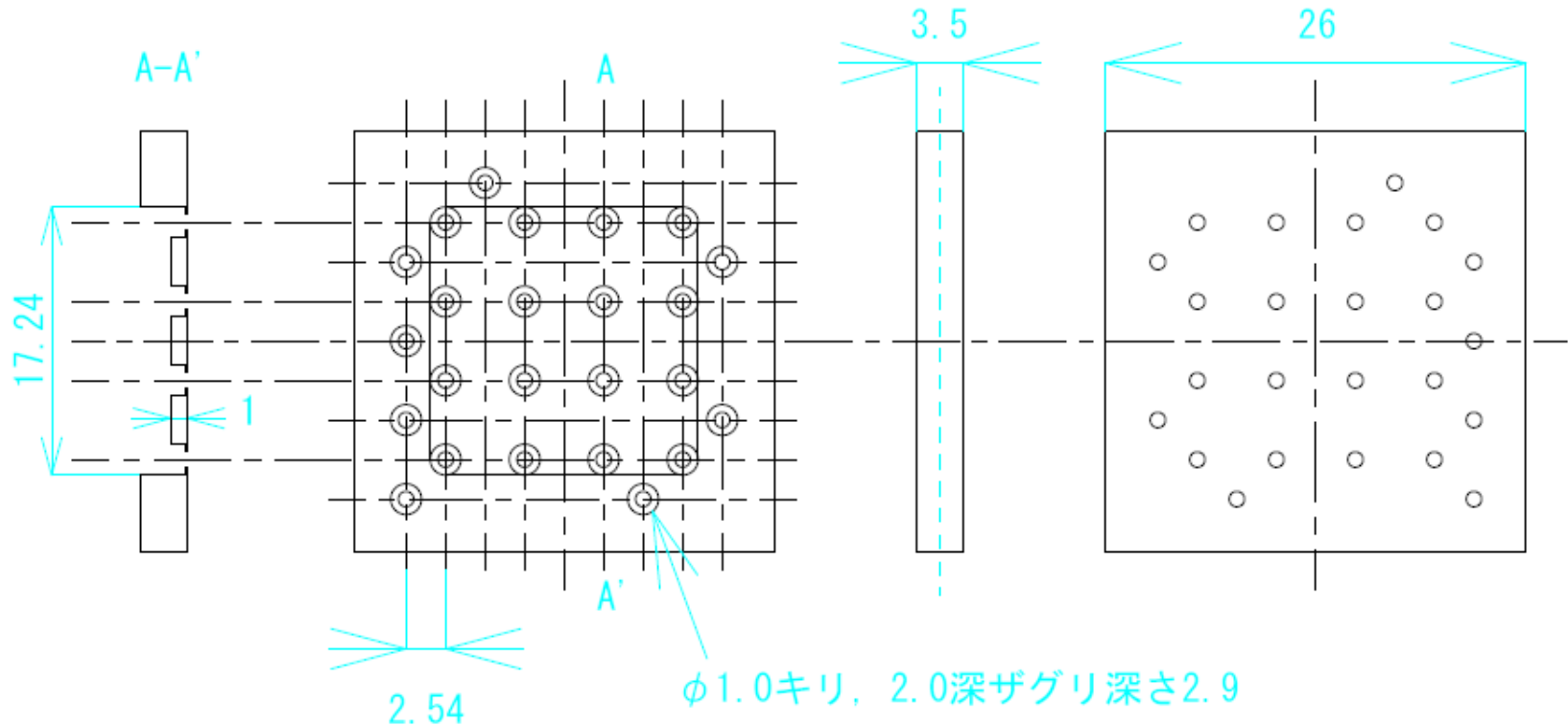
- Parts
- HV cable (TELEDYNE/REYNOLDS 178-5138)
- Capacitor (AVX 1808HC102KAT1A (1nF,3kV), 1206GC102KAT1A (1nF, 2kV))
- Socket, pin (MAC8)
 - Layer1: PD-1, WD-6, MX-1-1, PK-2-2(cut)
 - Layer2: WD-8, PD-7
 - Layer3: ME-1-1, TH-1.6-M2-B
- 2mm pitch pin
- PEEK screw (M2, 5mm, 6~8mm), nut, washer(3mm)

HV input



Socket cover (G10)

Discharge protection between HV pins



Test of version3

- With test pulse
- Signal $\sim 80\text{mV}$
- Cross-talk $\sim 8\text{mV}$
 - Slightly improved from version2

1			
2	3		



- Timing resolution, HV stability looks OK.
 - (See report on 4ch merged CFD readout)
- Issues from current CFD readout
 - Difficulty on treatment of HV cable routing
 - Need bundled cable or board connection
- Issues for final form factor
 - Total thickness
 - Connector between the socket and backplane

