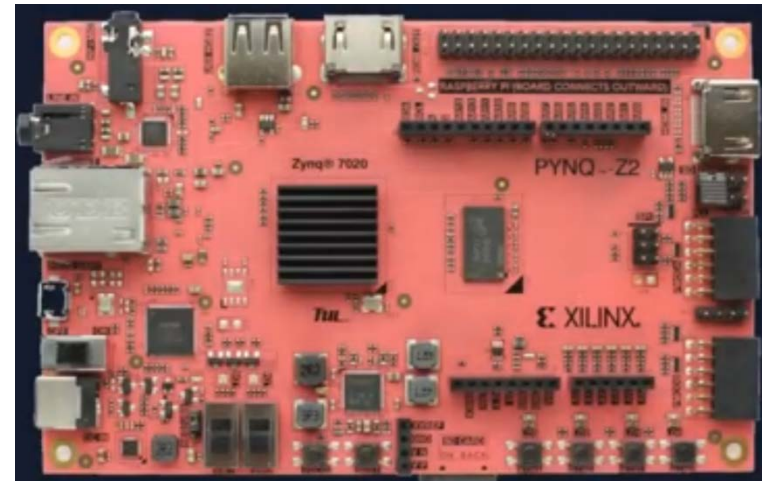


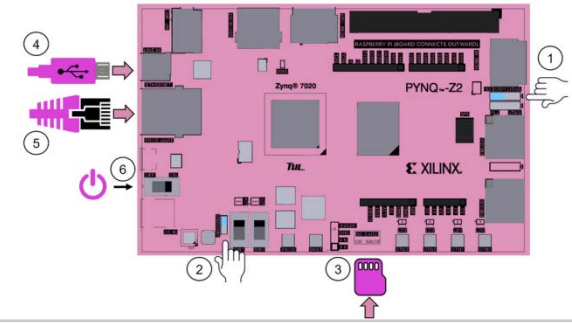
Gary Varner (PYNQ, IRSX)

• Progress

- Proposal Xilinx for new dev boards
- Received max (5) last week
- Will use in PHYS475 class
- Zynq architecture
 - Python Zynq (PYNQ) Jupyter Notebook



192.168.153.217:9090/tree?



• Plans

- Complete icarus verilog IRSX simulation
- Compare with IRSX measurements (Oskar)

20-JUL-2020

